

**IN THE SPECIFICATION:**

Please amend the specification by inserting the following paragraph on page 1, beginning on line 9, as shown here in clean copy:

COMMUNAL LOCK PROCESSING SYSTEM FOR MULTIPROCESSOR COMPUTER  
SYSTEM

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**RELATED APPLICATIONS:**

This is related to copending United States Patent Applications Serial No. 09/925,384 (Attorney's Docket No. RA-5336) and 09/927,069 (Attorney's Docket No. RA-5415) in that they share much of the same disclosure but claim related inventions.

**BACKGROUND OF THE INVENTION**

1. Field of the Invention

This invention relates generally to multiprocessor computer system architecture and more particularly to systems and methods for reducing access time to memory cells containing highly utilized locks in order to improve throughput.

2. Background Information

In U.S. Patent No. 6,052,760, issued to Bauman et al, (and commonly assigned to Unisys Corporation with the instant patent and hereby incorporated herein in its entirety by this reference), a system for providing identifiable software locks in a multiprocessor